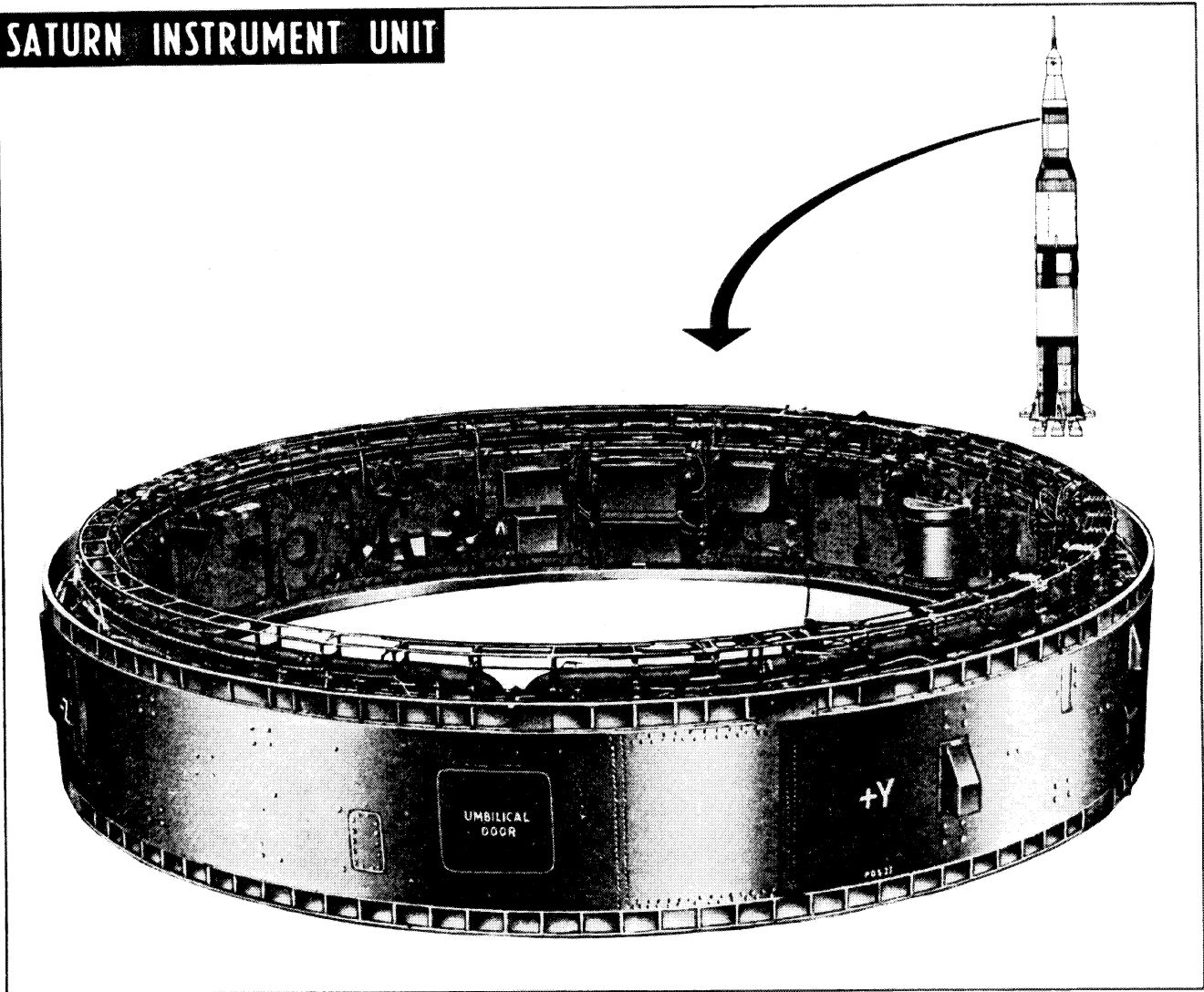


SATURN INSTRUMENT UNIT



INSTRUMENT UNIT EQUIPMENT LOCATIONS

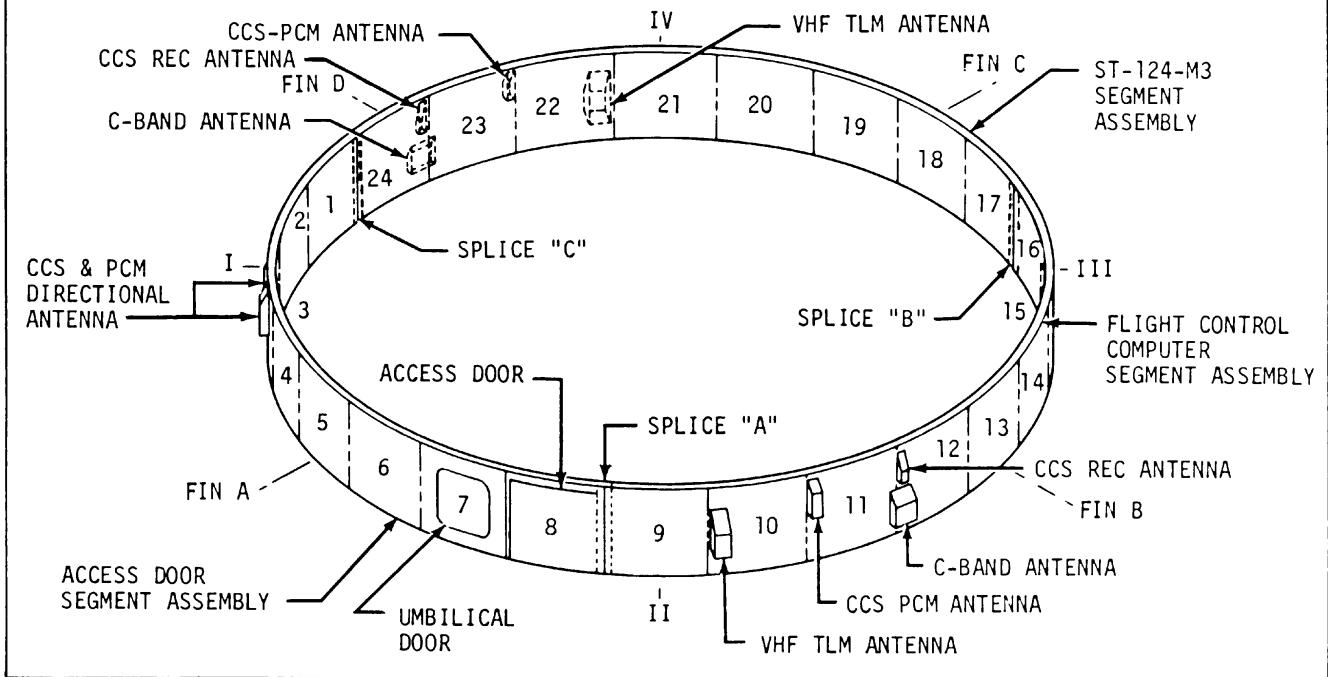


Figure 7-4 (Sheet 1 of 5)

INSTRUMENT UNIT EQUIPMENT LOCATIONS

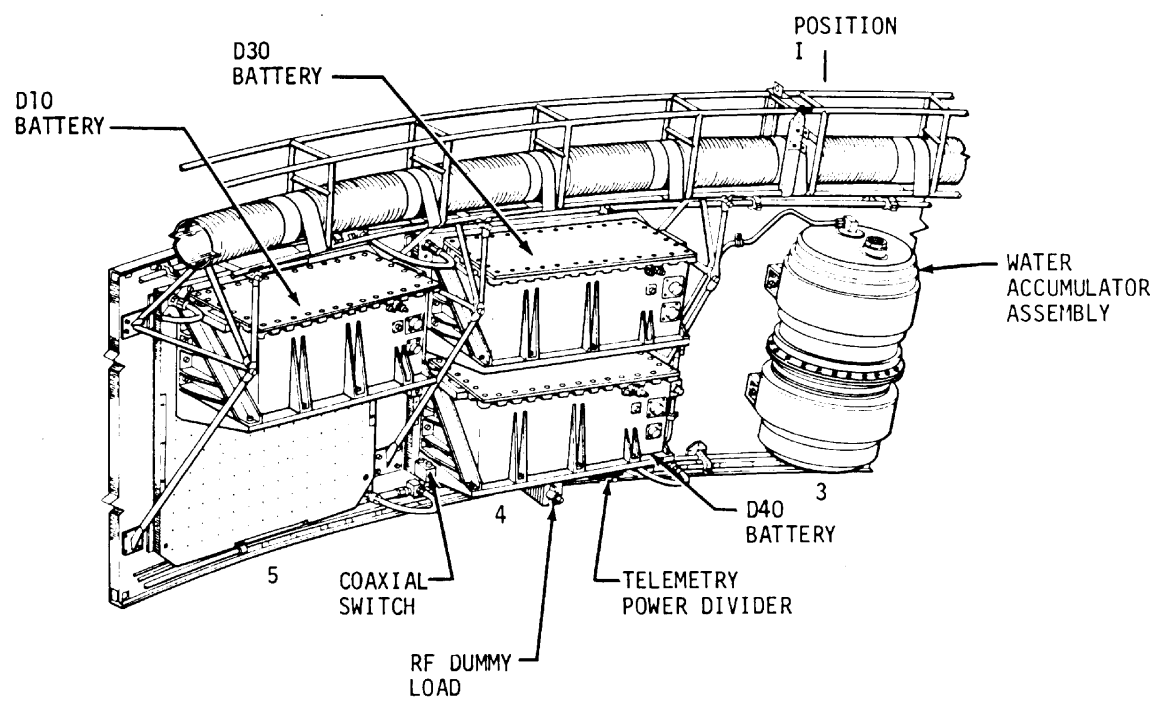
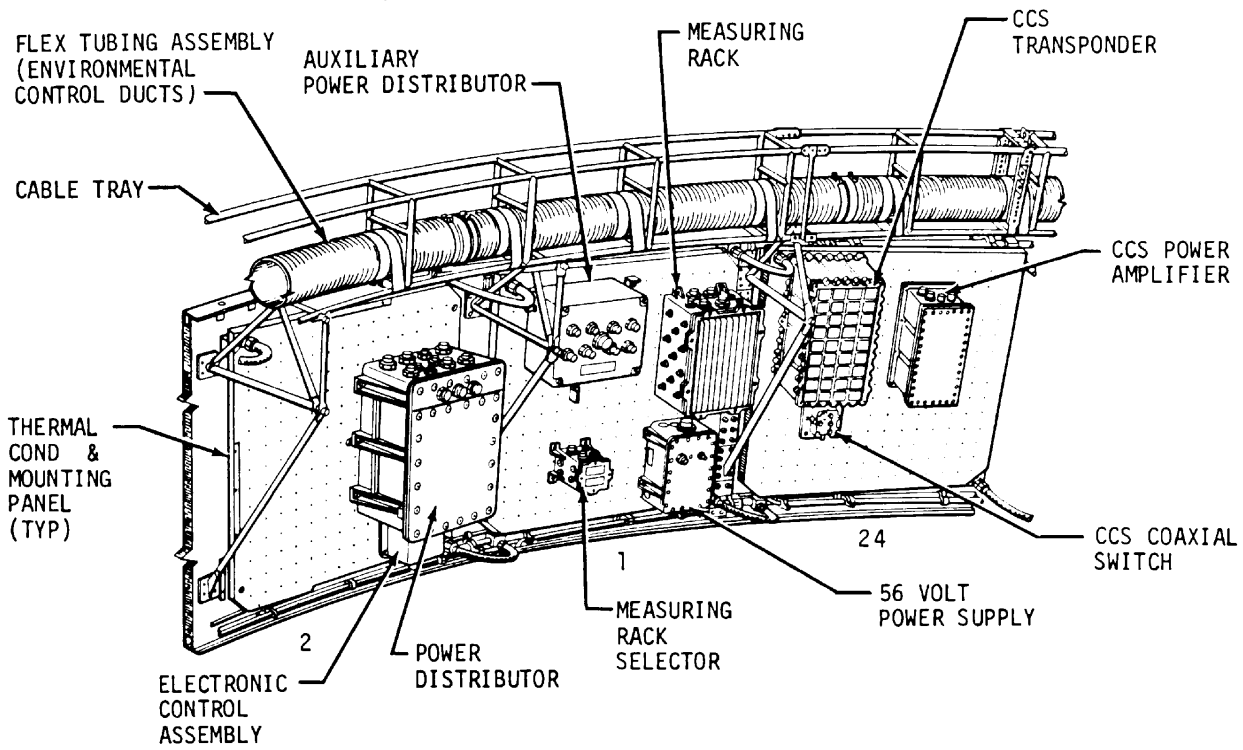


Figure 7-4 (Sheet 2 of 5)

INSTRUMENT UNIT EQUIPMENT LOCATIONS

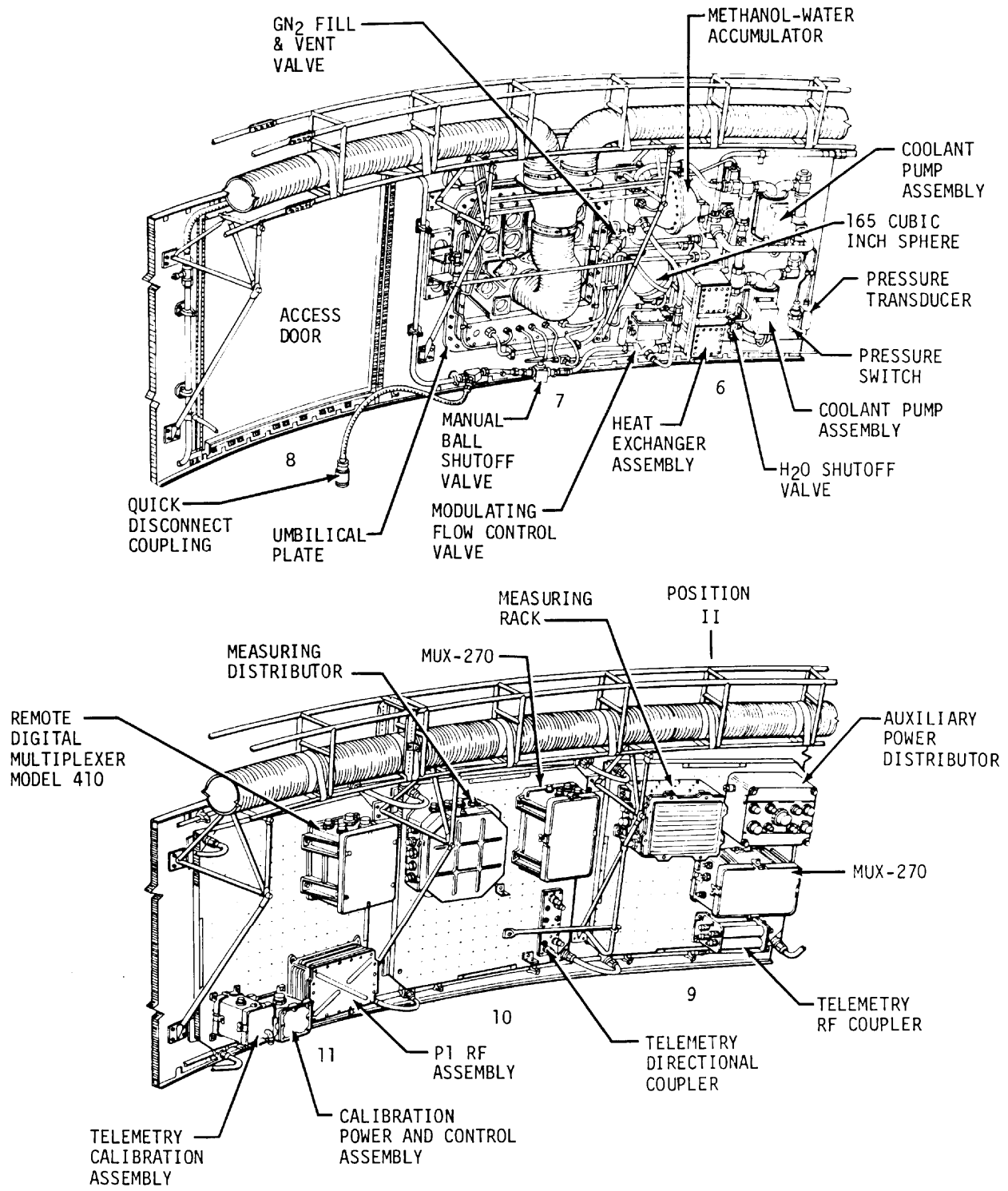


Figure 7-4 (Sheet 3 of 5)

INSTRUMENT UNIT EQUIPMENT LOCATIONS

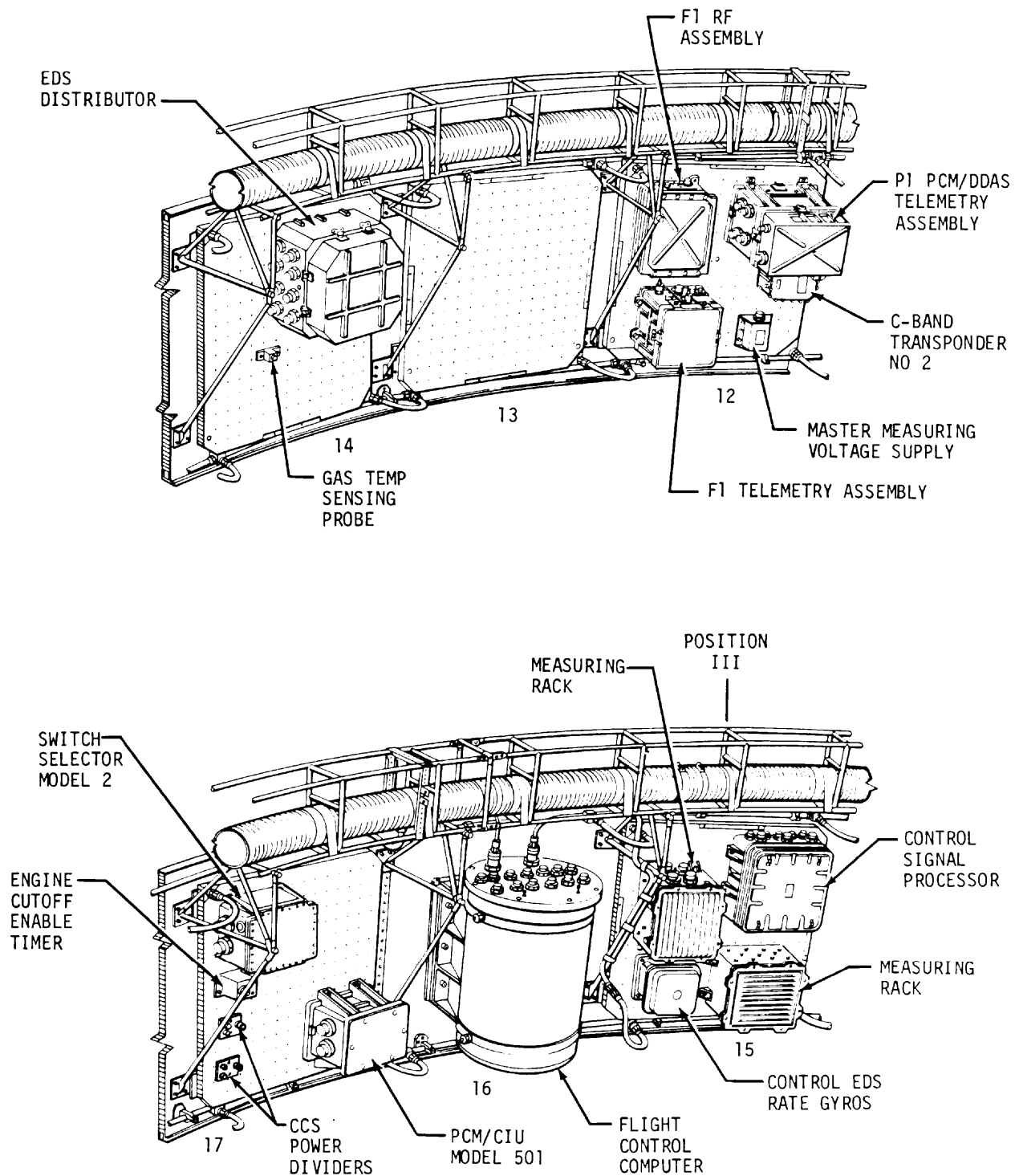


Figure 7-4 (Sheet 4 of 5)

INSTRUMENT UNIT EQUIPMENT LOCATIONS

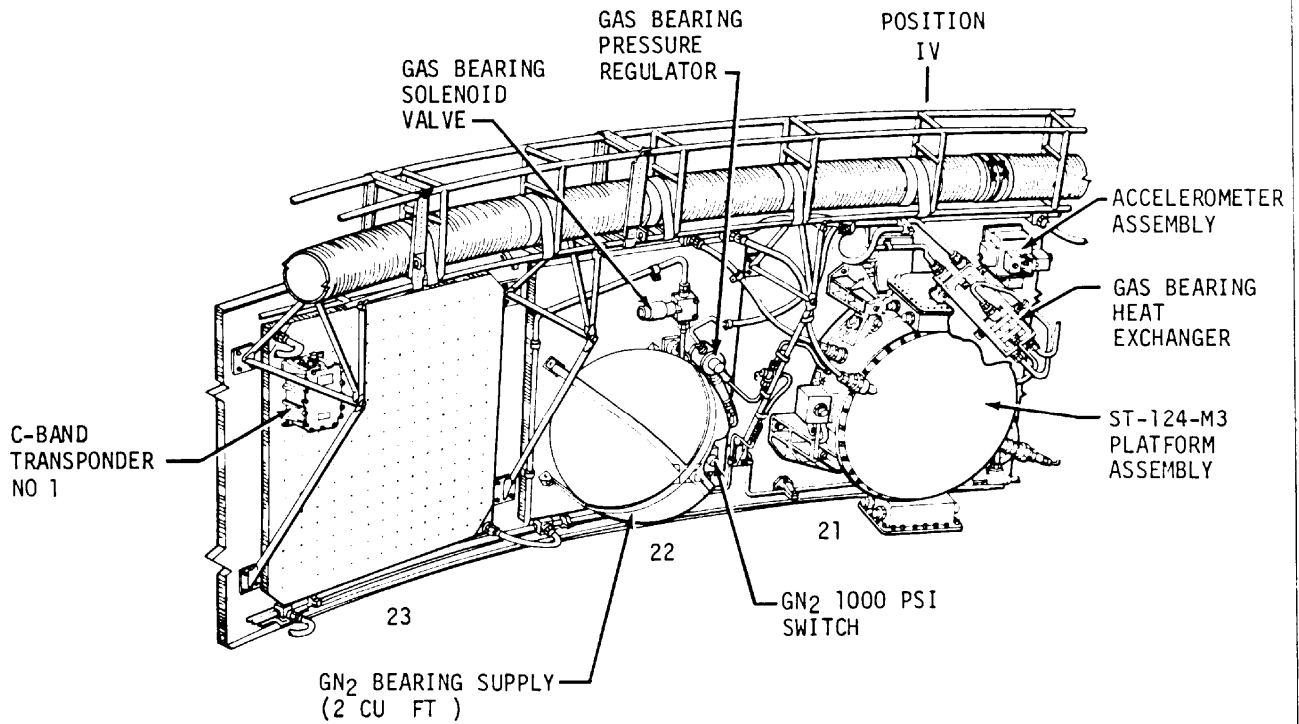
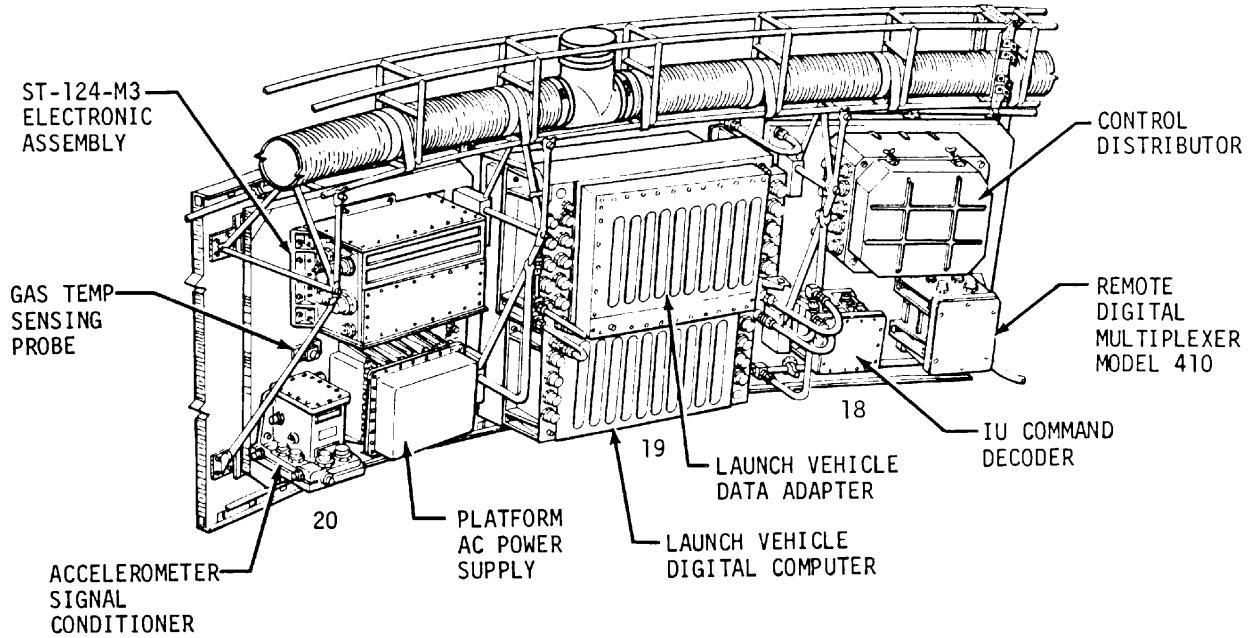
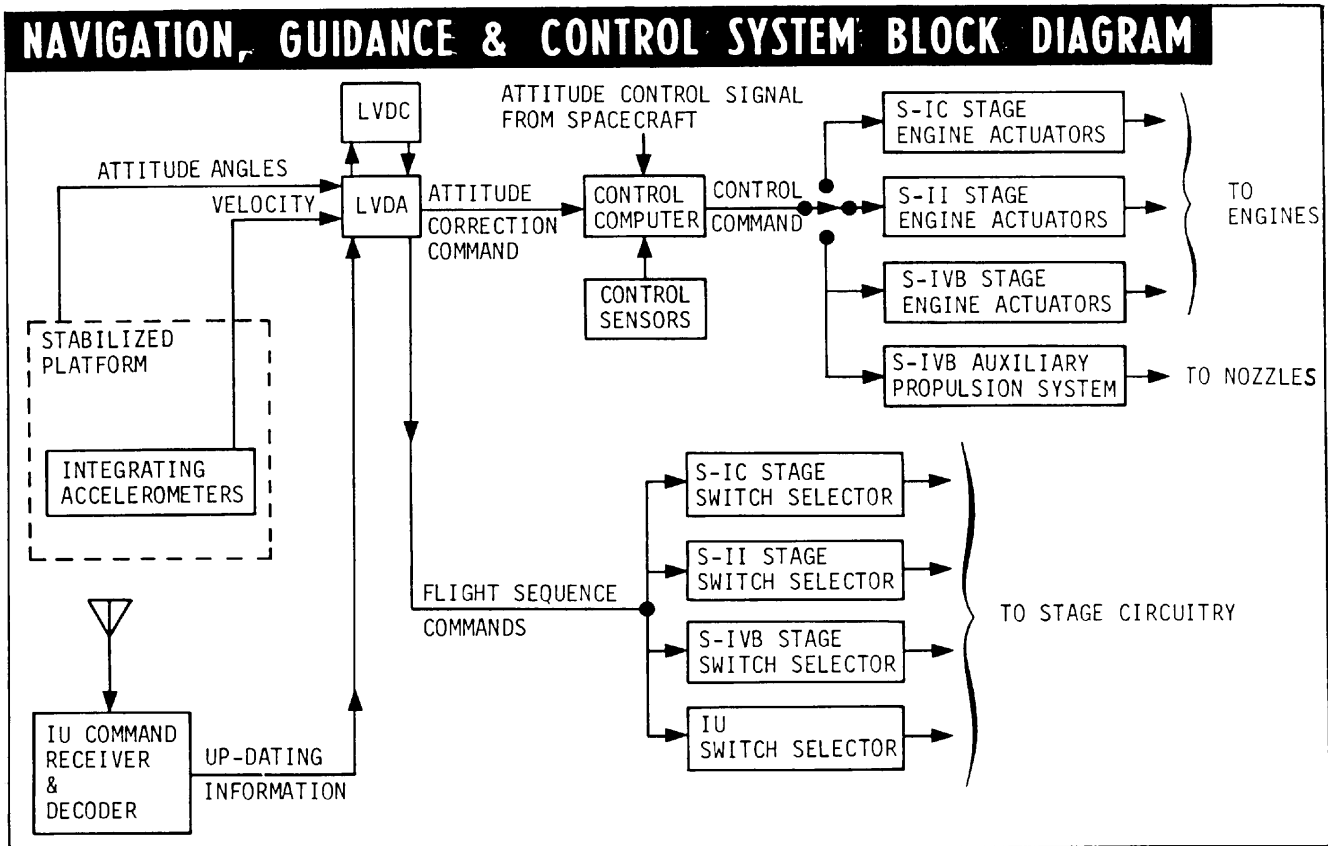


Figure 7-4 (Sheet 5 of 5)



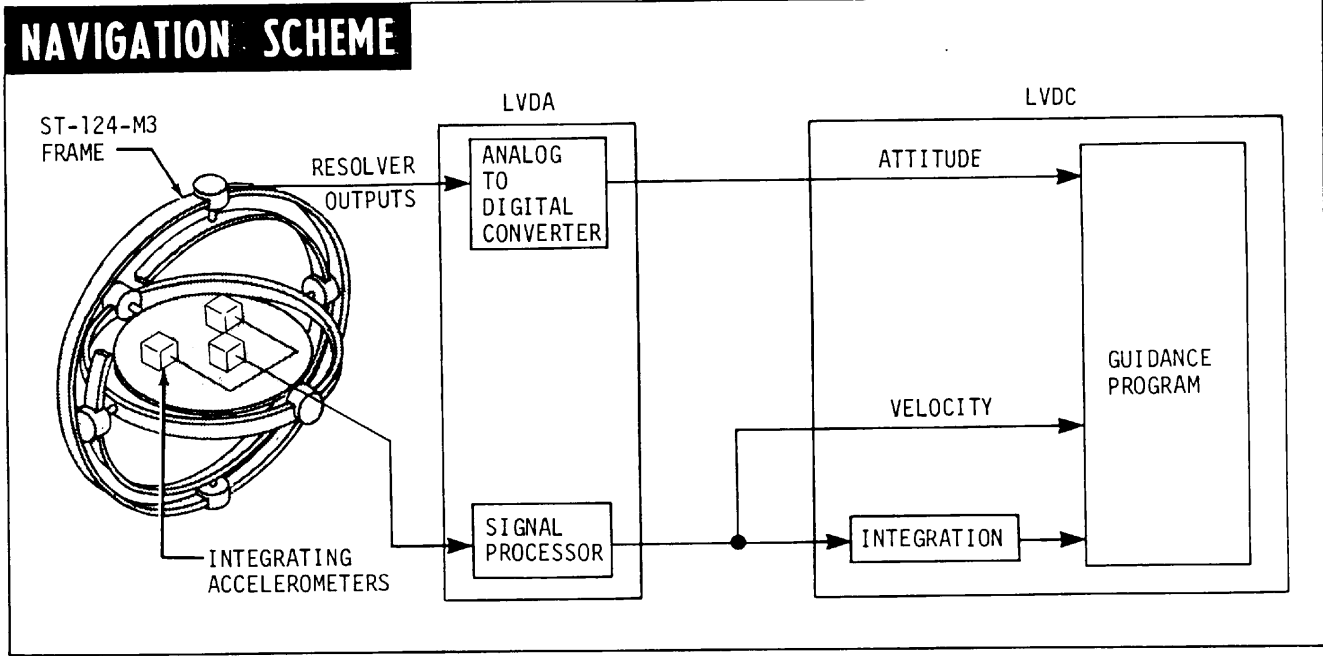
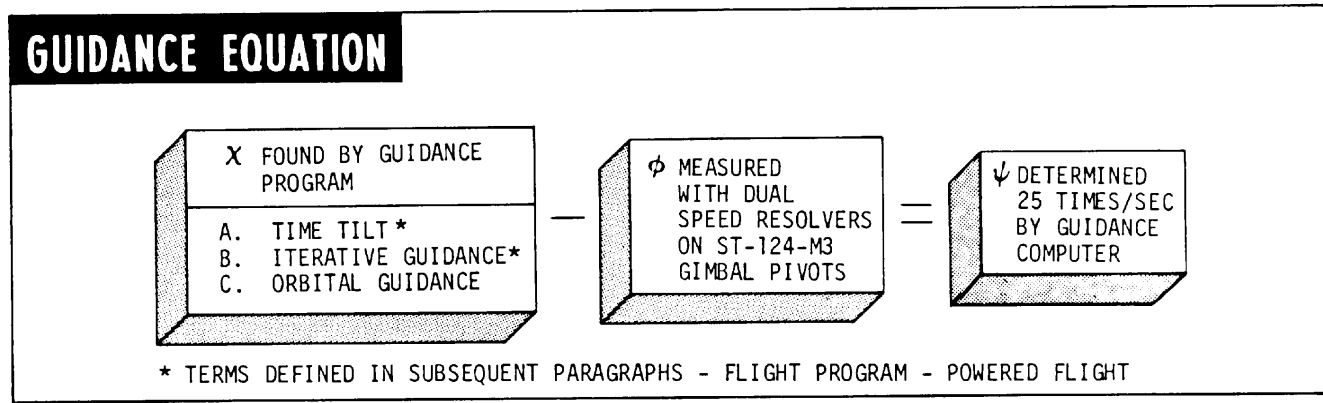


Figure 7-12



NASA Apollo Saturn V Rocket Summary Information

LAUNCH VEHICLE DIGITAL COMPUTER CHARACTERISTICS	
ITEM	DESCRIPTION
Type	General Purpose, Digital, Stored Program
Memory	Random Access, Ferrite (Torodial) Core, with a Capacity of 32,768 words of 28 Bits each
Speed	Serial Processing at 512,000 Bits Per Second
Word Make-Up	Memory = 28 Bits Data = 26 Bits Plus 2 Parity Bits Instruction = 13 Bits Plus 1 Parity Bit
Programming	18 Instruction Codes 10 Arithmetic 6 Program Control 1 Input/Output 1 Store
Timing	Computer Cycle = 82.03 μ sec. Bit Time = 1.95 μ sec. Clock Time = 0.49 μ sec.
Input/Output	External, Program Controlled

LAUNCH VEHICLE DATA ADAPTER CHARACTERISTICS	
ITEM	DESCRIPTION
Input/Output Rate	Serial Processing at 512,000 Bits Per Second
Switch Selector	8 Bit Input 15 Bit Output
Telemetry Command Receiver	14 Bits for Input Data
Data Transmitter	38 Data and Identification Bits Plus Validity Bit and Parity Bit
Computer Interface Unit	15 Bits Address Plus 1 Data Request Bit 10 Bits for Input Data Plus 1 Bit for Data Ready Interrupt
Delay Lines	3 Four-Channel Delay Lines for Normal Operation 1 Four-Channel Delay Line for Telemetry Operations
Output to Launch Computer	41 Data and Identification Bits Plus Discrete Outputs
Input From RCA-110 GCC	14 Bits for Data Plus Interrupt

LAUNCH COMPLEX 39

